

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Title: Semiconductor Device and Manufacturing Method Thereof
 Inventors: Hideyuki ARAKAWA
 Atty Docket No.: 401346
 Leydig, Voit & Mayer, Ltd. 202-737-6770

FIG.1

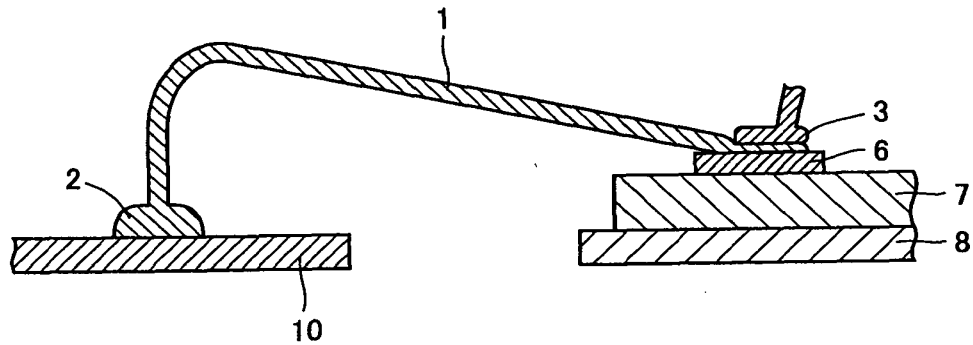


FIG.2

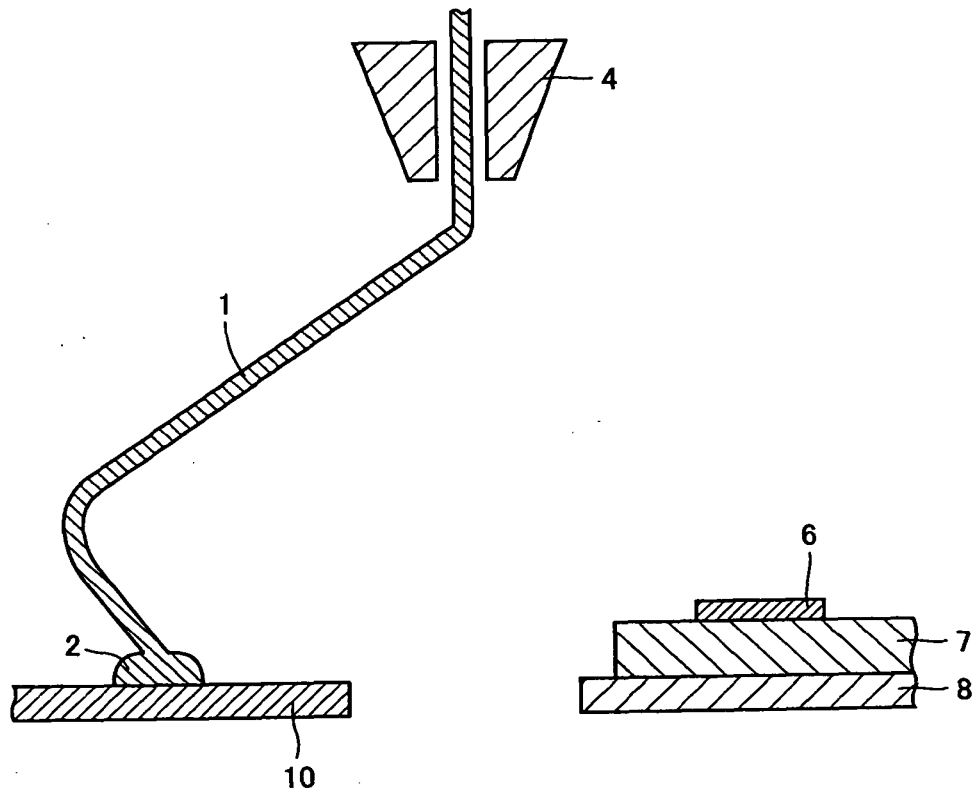


FIG. 280" E194E660

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FIG.3

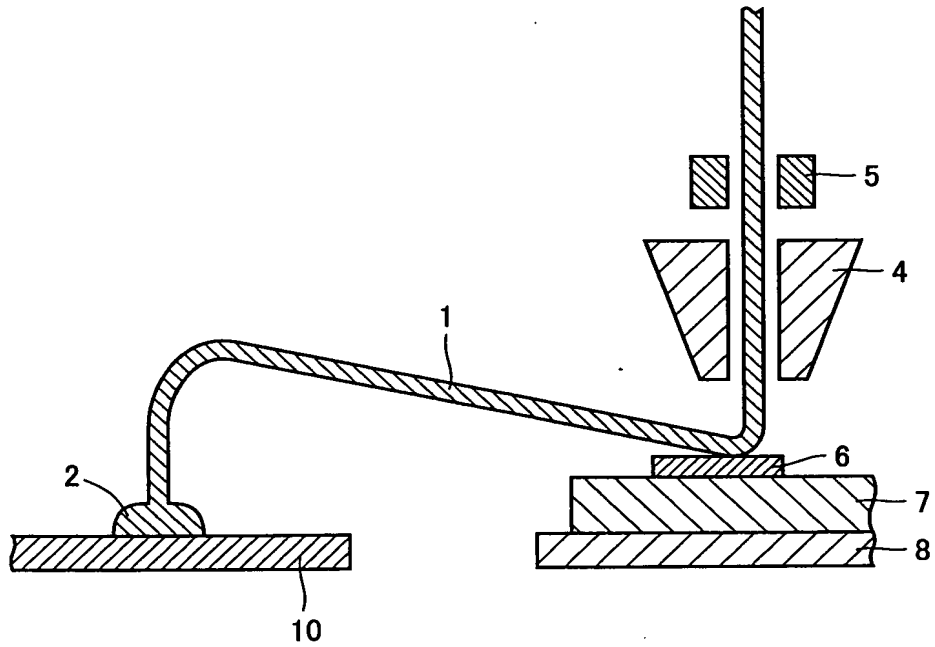


FIG. 3

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FIG.4

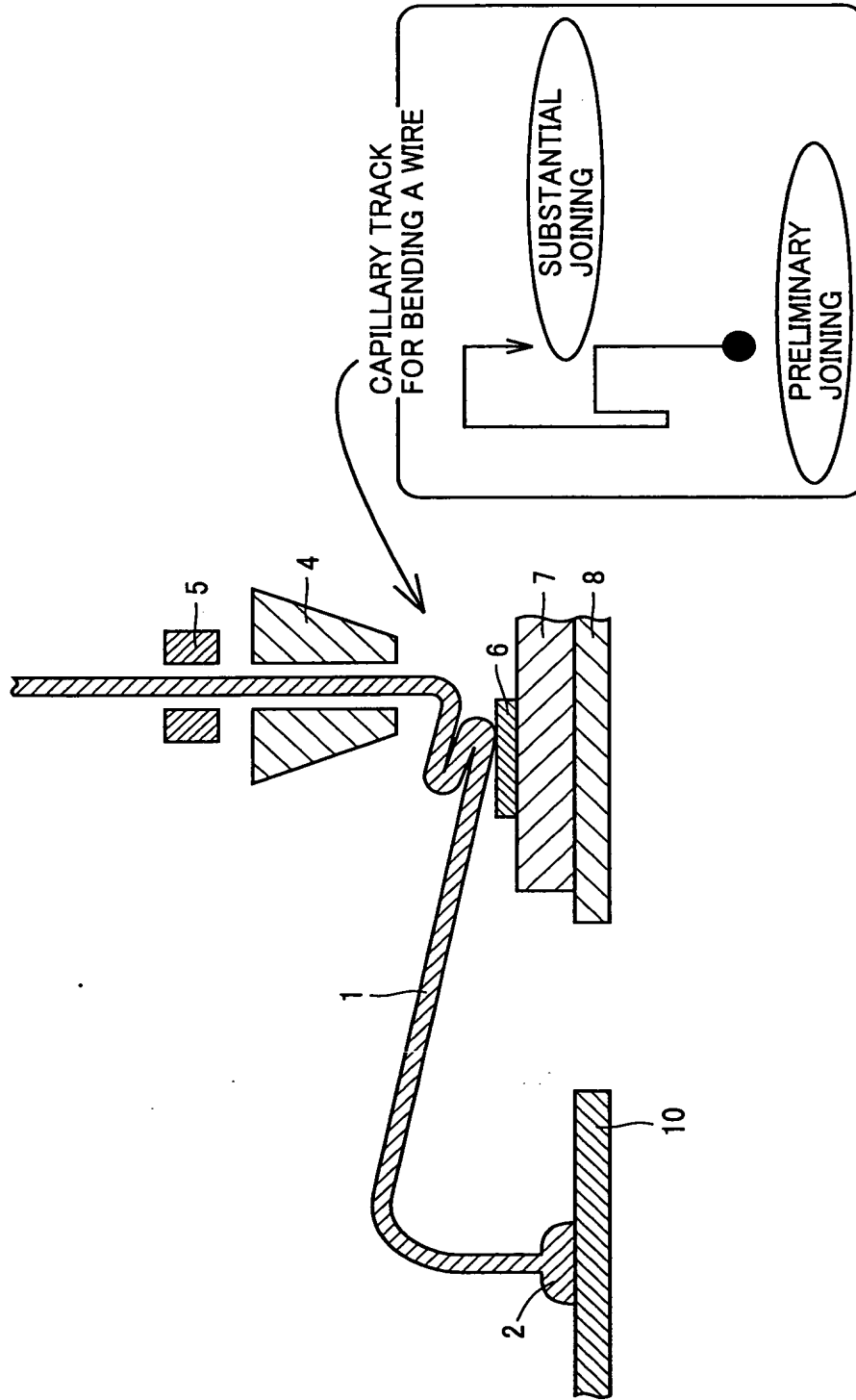


FIG.4

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FIG.5

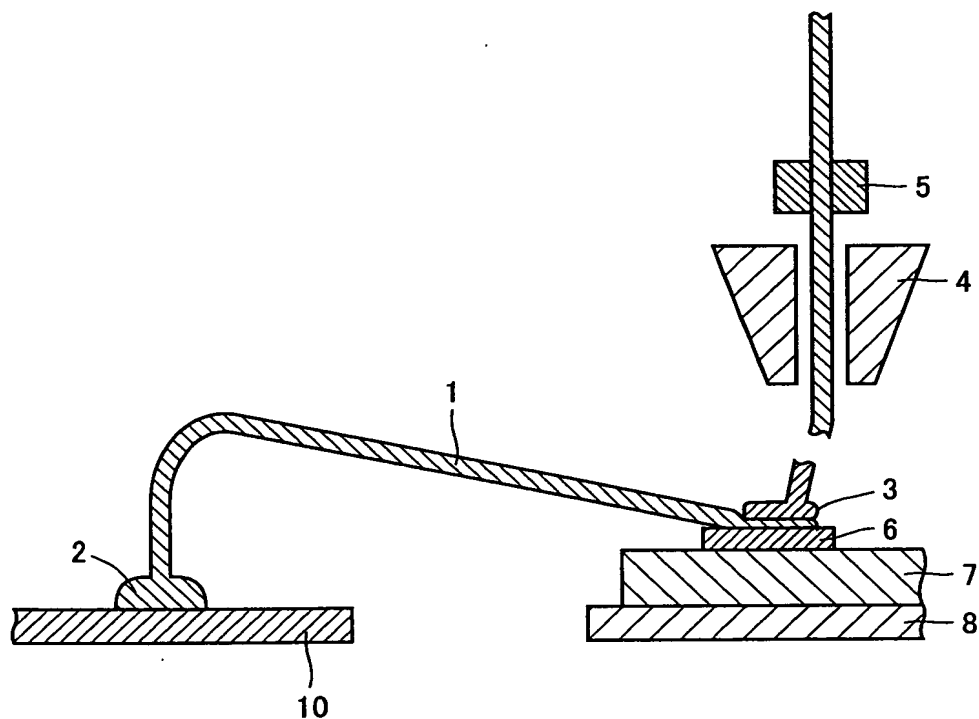


FIG. 5

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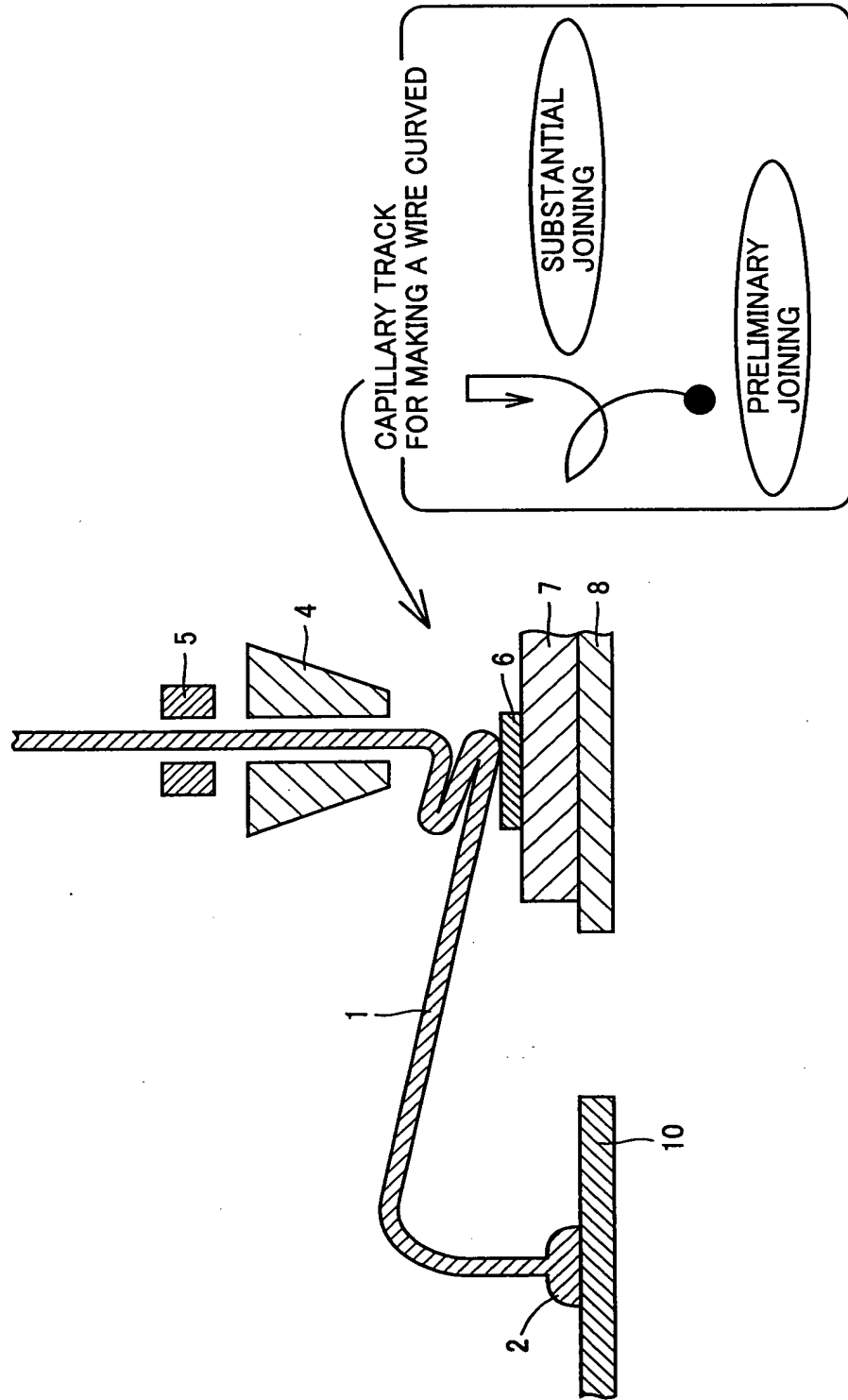


FIG.6

FIG. 6

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FIG.7A

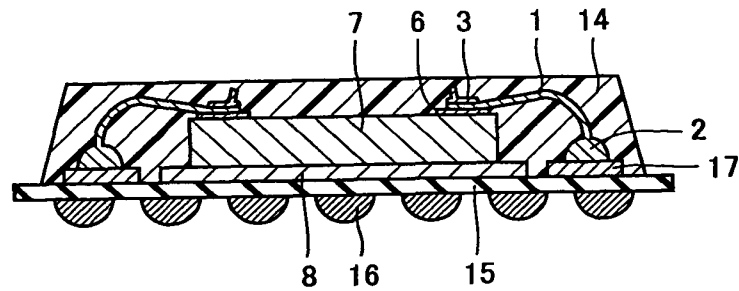


FIG.7B

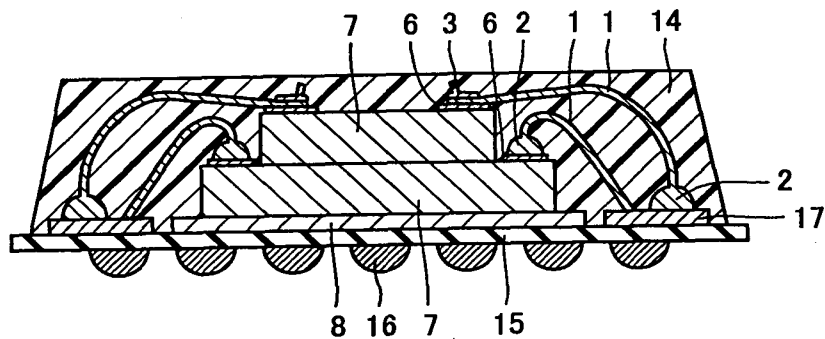


FIG.7C

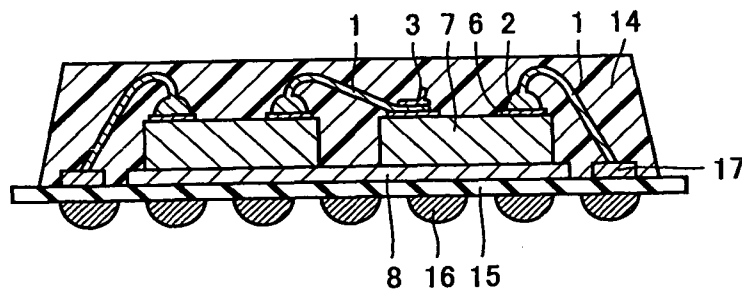


FIG. 7A

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FIG.8 PRIOR ART

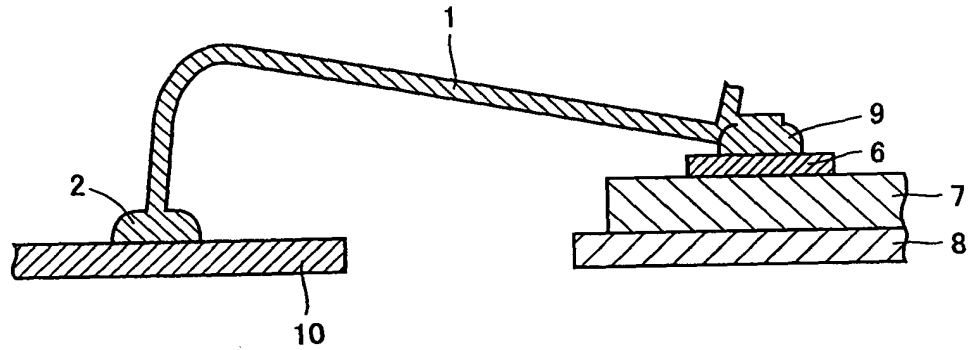
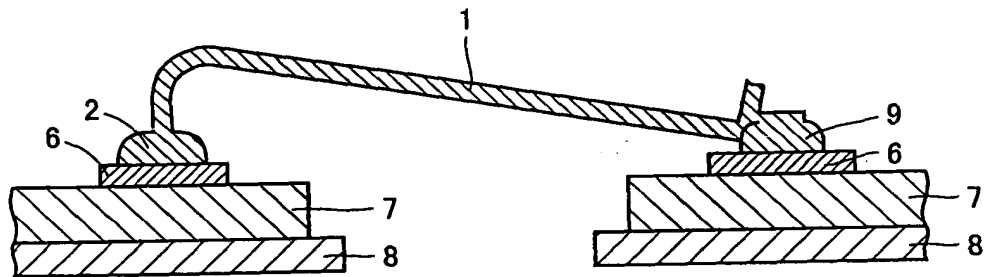


FIG.9 PRIOR ART



0934643-08301

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FIG.10 PRIOR ART

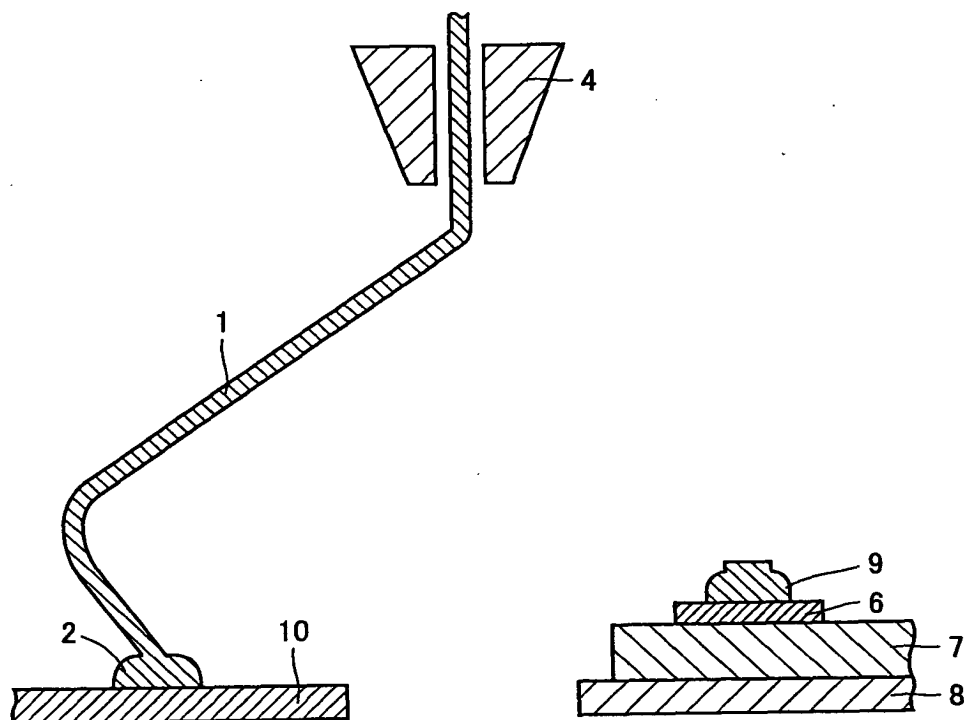
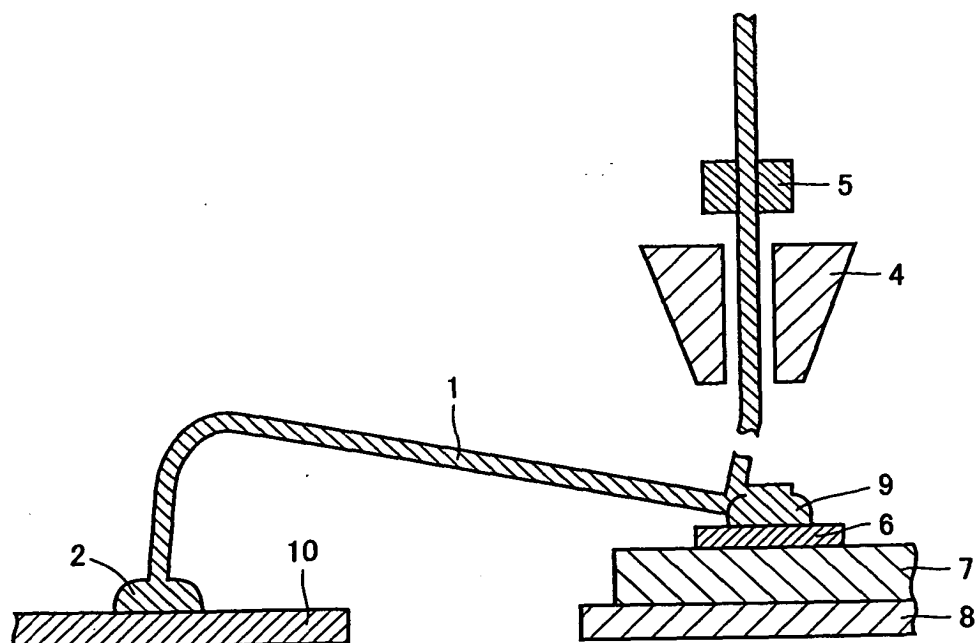


FIG.11 PRIOR ART



09934643 082301

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FIG.12 PRIOR ART

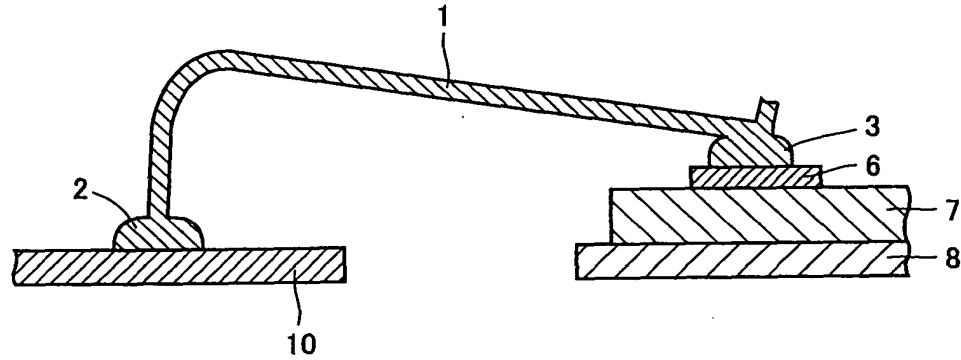


FIG.13 PRIOR ART

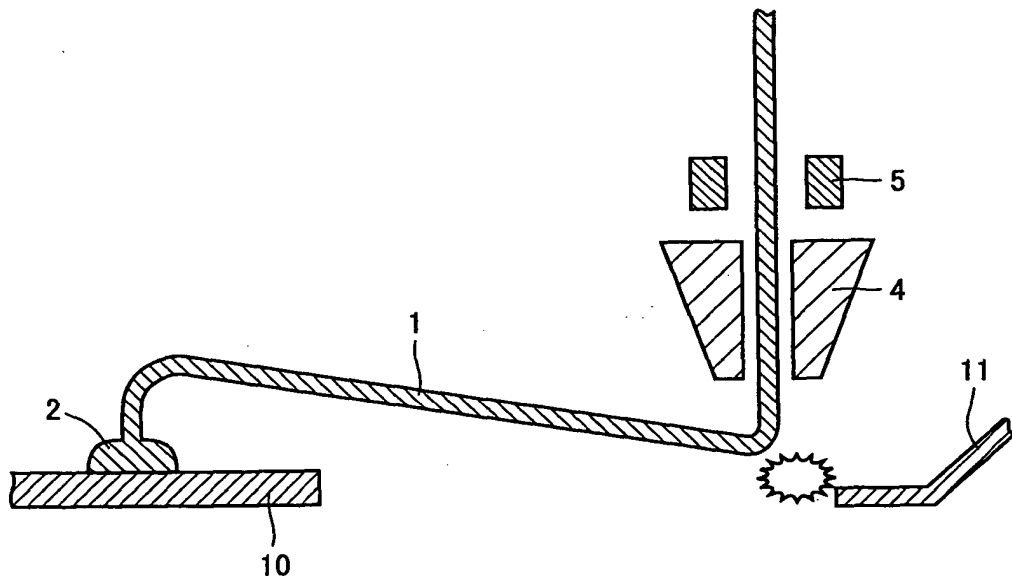


FIG.12 PRIOR ART

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FIG.14 PRIOR ART

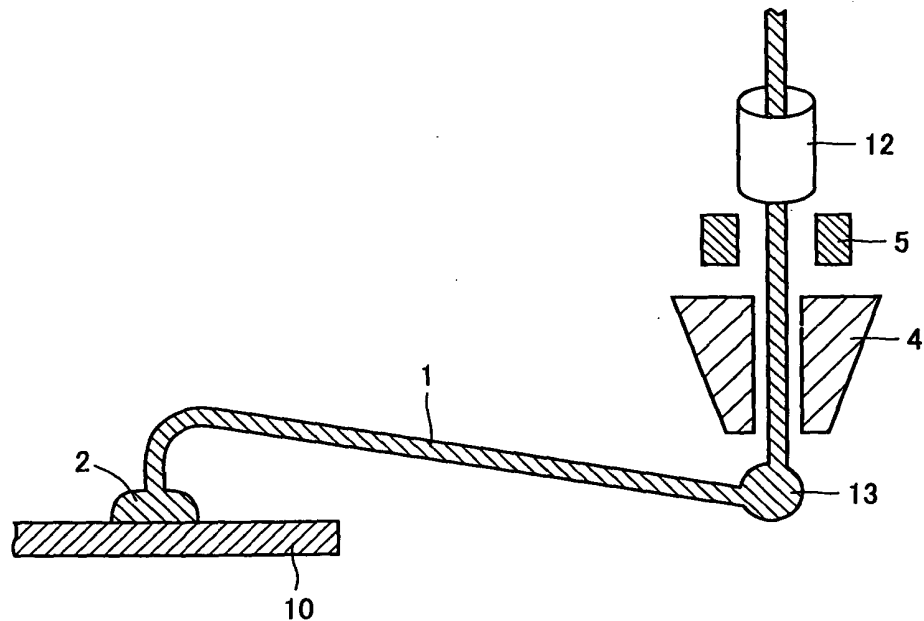


FIG.15 PRIOR ART

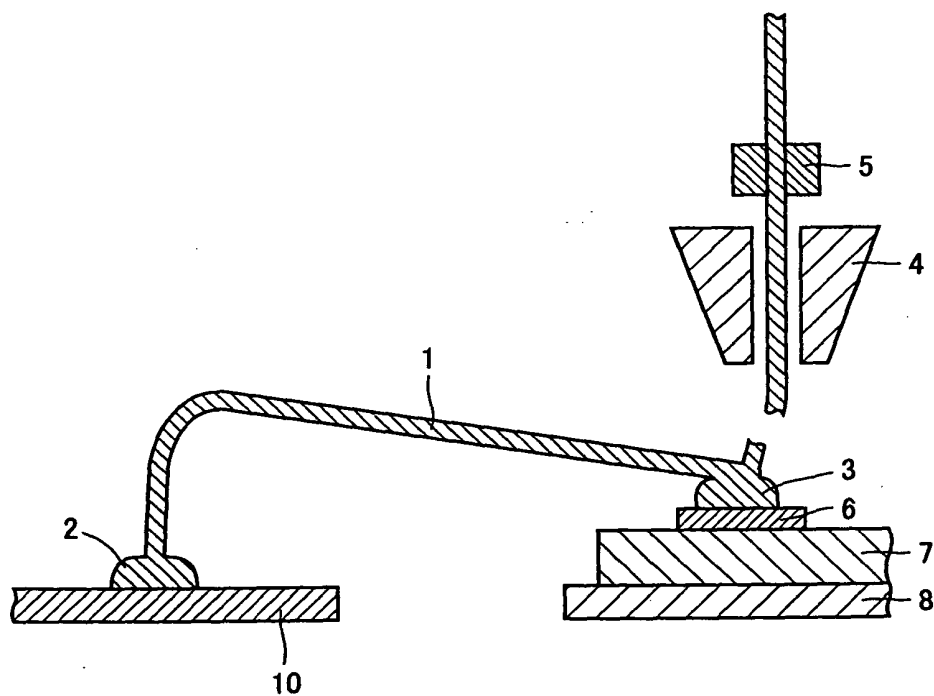


FIG.14 PRIOR ART